



## Signal Propagation on Interconnects

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This book comprises a selection of the most representative contributions to the 1st IEEE Workshop on Signal Propagation on Interconnects that was held at Travemunde, Germany, in May 1997. It represents, therefore, a survey of the actual problems currently concerning researchers and professionals in the field of signal propagation on interconnects. Signal Propagation on Interconnects contains chapters which cover a wide area of important research results dealing with simulation and measurement of noise and radiated emissions on boards, describing ground bounce effects as well as inductance calculations in multilayer packages. There is also reference and coverage of timing simulation techniques on chip as well as on board level. Signal Propagation on Interconnects is intended to give developers and researchers in the field of chip and package design a review of the state of the art regarding the influence of interconnect effects on the electrical performance of electronic circuits. In addition, the book illustrates ways to overcome performance problems related to the parasitic influences of interconnects. It is an invaluable text for circuit design engineers, developers and researchers in the field of signal integrity.

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